

Applicants:

Jay Sethuram, Richard Weber, and Chandra Shekh

Assignee:

Cisco Technology, Inc.

Title:

Method And System For Source Synchronous Clocking

Serial No.:

09/450,802

Filing Date:

November 29, 1999

Examiner:

Duc T. Duong

Group Art Unit:

2663

Docket No.:

CIS0089US

Client Ref. No.:

1967

Austin, Texas June 30, 2004

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

RECEIVED

JUL 0 9 2004

Technology Center 2600

RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Office Action dated May 24, 2004, having a shortened statutory period expiring on August 24, 2004. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

1

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Amendments to the Drawings begin on page 7 of this paper.

Remarks begin on page 8 of this paper.

Serial No.: 09/450,802